

Express Mail No. EV355037520US
PATENT

IBM/174DV1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John Michael Borkenhagen et al. Art Unit: Unknown
Serial No.: FILED HEREWITH Examiner: Unknown
Filed: September 12, 2003 Atty. Docket No.: IBM/174DV1
For: DATA STROBE GATING FOR SOURCE SYNCHRONOUS
COMMUNICATIONS INTERFACE

INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure under the provisions of 37 C.F.R.

§ 1.56, the references listed on the enclosed Form PTO-1449 are brought to the attention of the Examiner. Each of the references was cited in the parent for the above-identified application, U.S. Serial No.09/836,651, filed on April 17, 2001. Accordingly, no copies of the references are provided.

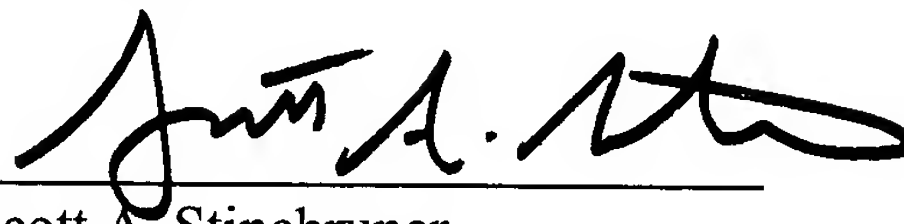
The Examiner will also note that this application may also be related to co-pending U.S. Patent Application Serial No. 09/617,558 filed on July 17, 2000 by Borkenhagen et al.

No representation is made that any of the references are "prior art" within the meaning of 35 U.S.C. § 102 or § 103. Moreover, any explanations herein are not to be taken as a representation that the references have been thoroughly reviewed. In particular, no representation as to the relative relevance of any portion of a reference is intended.

Consideration of the references and the remarks presented herein in the above-identified application is respectfully requested.

If there are any questions regarding this paper, or which might otherwise further this case onto allowance, please contact the undersigned at (513) 241-2324. Moreover, if any other charges or credits are necessary to complete this communication, please apply them to Deposit Account 23-3000.

Respectfully submitted,



Scott A. Stinebruner

Reg. No. 38,323

WOOD, HERRON & EVANS, L.L.P.

2700 Carew Tower

441 Vine Street

Cincinnati, Ohio 45202

(513) 241-2324

12 SEP 2003
Date

SUBSTITUTE FORM PTO-1449 (MODIFIED)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. IBM/174DV1		SERIAL NO. FILED HEREWITH							
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANT John Michael Borkenhagen et al.									
(37 CFR 1.98(b))				FILING DATE September 12, 2003		GROUP							
U.S. PATENT DOCUMENTS													
EXAMINER INITIAL		PATENT NUMBER						ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	A.A	6	1	4	7	9	2	7	11/14/2000	Ooishi	365	233	6/3/1999
	A.B	6	2	1	9	7	4	6	4/17/2001	Vogley	711	104	12/6/1999
	A.C	6	3	1	6	9	8	0	11/13/2001	Vogt et al.	327	273	6/30/2000
	A.D	6	4	5	6	5	4	4	9/24/2002	Zumkehr	365	193	3/30/2001
	A.E												
	A.F												
	A.G												
	A.H												
	A.I												
	A.J												
	A.K												
FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS													
		DOCUMENT NUMBER						PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION (YES/NO)	
	A.L												
	A.M												
	A.N												
	A.O												
	A.P												
	A.Q												
OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)													
	A.R	"Double Data Rate (DDR) SDRAM Specification", JEDEC Standard, JESD79, JEDEC Solid State Technology Association 2000, Arlington, VA, (6/2000).											
	A.S	U.S. Patent Application Serial No. 09/617,558, entitled "Programmable Compensated Delay for DDR SDRAM Interface," filed on 7/17/2000 by Borkenhagen et al. (ROC920000157US1).											
	A.T												
EXAMINER									DATE CONSIDERED				
EXAMINER: Initial if citation considered, whether or not in conformance. Draw line through citation only if not in conformance <u>and</u> not considered. Include a copy of this form with next communication to applicant.													